



Pinout diagram for CD74HC4050EE4 showing connections for SCLK, MOSI, LCDCS, and power pins.

Pin	Signal	Connection
1	VCC	+3.3V
2	1Y	SCLK_3V
3	1A	SCLK_5V
4	2Y	MOSI_3V
5	2A	MOSI_5V
6	3Y	LCDCS_3V
7	3A	LCDCS_5V
8	GND	GND

Additional pins shown on the right side of the chip:

- 16: NC
- 15: 6Y (connected to DC_3V)
- 14: 6A (connected to DC_5V)
- 13: NC
- 12: 5Y (connected to RESET_3V)
- 11: 5A (connected to RESET_5V)
- 10: 4Y (connected to SDCS_3V)
- 9: 4A (connected to SDCS_5V)



Diagram of the J1 connector pinout. The connector has 16 pins. Pins 1, 3, 5, 7, 9, 11, 13, and 15 are connected to EXTRA_1 through EXTRA_15 respectively. Pins 2, 4, 6, 8, 10, 12, 14, and 16 are connected to EXTRA_2 through EXTRA_16 respectively. The connector is labeled J1 and Conn_02x08_Odd_Even.

