

2023 COMPUTER ENGINEERING: Digital VLSI Circuits Track

DEGREE REQUIREMENT CHECKSHEET

COLLEGE OF ENGINEERING & COMPUTER SCIENCE

UNIVERSITY OF CENTRAL FLORIDA

GENERAL EDUCATION PROGRAM				LOWER AND JUNIOR LEVEL REQUIRED COURSES			SH	Grd	Trans Equiv
* Indicates "C-" minimum required by the Gordon Rule				EGS 1006C Introduction to the Engineering Profession			1	#	
** Indicates minimum "C" or better grade				EGN 1007C Engineering Concepts and Methods			1	#	
COMMUNICATION (9 SEM HRS)		SH	Grd	Trans Equiv	STA 3032 Probability & Statistics for Engineers			GEP	
ENC 1101	3	*		EEL 3926L Junior Design			1		
ENC 1102	3	*		EGN 3211 Engineering Analysis & Computation			3	**	
				EEL 3004C Linear Circuits I			3	**	
SPC 1603C	3			EEL 3123C Linear Circuits II			3	**	
CULTURAL & HISTORICAL (9 SEM HRS)				EEE 3307C Electronics I			4		
Select 2: AMH 2010, EUH 2000, EUH 2001, HUM 2211, HUM 2230, WOH 2012, WOH 2022				EEE 3342C Digital Systems			3	**	
Approved Cultural Foundations course:				EEL 3801C Computer Organization			4	**	
SOCIAL FOUNDATION - (6 SEM HRS)				COT 3100C Introduction to Discrete Structures			3	**	
ANT 2000/ PSY 2012/ SYG 2000	3			COP 3330 Object Oriented Programming			3	**	
ECO 2013 <u>or</u> ECO 2023	3			COP 3502C Computer Science I			3	**	
SCIENCE - 6 SH				COP 3503C Computer Science II			3	**	
GEO 1200 <u>or</u> GEO 2370 (either GEO is preferred) <u>or</u> BSC 1050C <u>or</u> BSC 1005C <u>or</u> GLY 1030				SENIOR LEVEL REQUIRED COURSES			SH	Grd	Trans Equiv
PHY 2048C General Physics Using Calculus I	4			EEL 4742C Embedded Systems			3		
MATHEMATICAL - 6 SH				EEL 4768 Computer Architecture			3		
MAC 2311C Calculus with Analytic Geometry I	4	**		EEL 4781 Computer Communication Networks			3		
STA 3032 Probability & Statistics for Engineers	3			RECOMMENDED SENIOR LEVEL ELECTIVE COURSES			SH	Grd	Trans Equiv
GPA Gen Ed Prog =	36			(CHOOSE MINIMUM 2 FROM LIST)					
ENGINEERING CORE**				SH	Grd	Trans Equiv	EEE 4334 Computer-Aided Design of VLSI	3	
MAC 2311C Calculus with Analytic Geometry I	GEP	**		EEE 4346C Hardware Security and Trusted Circuit Design			3		
MAC 2312 Calculus with Analytic Geometry II	4	**		EEE 4775 Real-Time Systems			3		
MAC 2313 Calculus with Analytic Geometry III	4	**		EEL 4783 HDL in Digital Systems Design			3		
MAP 2302 Ordinary Differential Equations I	3	**		EEL 4798 Massive Storage and Big Data			3		
CHS 1440 Principles of Chemistry (or CHM 2045C)	4	**		EEL 5722C Field-Programmable Gate Array (FPGA) Design			3		
PHY 2048 & PHY 2048L General Physics Using Calculus I & Lab	GEP	**		REQUIRED					
PHY 2049 & PHY 2049L General Physics Using Calculus II & Lab	4	**		Technical Electives (see list of EEE/EEL/CDA/CAP/COP/COT/CNT)			15	~	
SUBTOTAL SEM HRS	19			EEL 4914 Senior Design I			3		
				EEL 4915L Senior Design II			3		
				SUBTOTAL SEM HRS			71		
				GPA Engr Option = (2.250 minimum)					
** A Grade of C (2.00) or higher required				ADVISOR COMMENTS:					
# Transfer students please see your faculty advisor before registering for these classes.				-BS-MS students should choose (3 SH) 5000 level courses as electives.					